

Output Levels



Figure 1-19: Active and passive states of a tri-state buffer.

Interpretation of Timing Diagrams



Figure 1-20: Timing diagram notation examples.

Analog to Digital Converters

- Sampling rate (MHZ)
- Sampling resolution (bits)
- Interface type (parallel or serial)
- Reference voltage (volt)
- Input range (unipolar or bipolar)
- Sample-and-hold circuit usually inside chip
- Note: Analog antialiasing filter must be in place

ADC Technologies



Parallel Interface Example







Memory

- External interface possible in most cases
- Nonvolatile EPROM
- Voltile RAM (SRAM, DRAM, etc.)
- Nonvolatile EEPROM
- Nonvolatile FLASH
- Refer to timing diagram for each to know how to interface with microcontroller

Temperature Sensors

Thermistors (PTC, NTC)

Typical NTC Thermistor Data

Temp °C	R/R_{25}	Temp °C	R/R ₂₅
50	20.02	20	9076
-50	39.03	30	.0270
-40	21.47	40	.6406
-30	12.28	50	.5758
-20	7.28	60	.4086
-10	4.46	70	.2954
0	2.81	80	.2172
10	1.82	90	.1622
20	1.21	100	.1229
25	1.00	110	.09446





Mechanical Switch



Motor Interfacing

Stepper motor

WAVEFORM FORWARD ROTATION



WAVEFORM REVERSE ROTATION COIL A ______





